

In the Claims

The following Listing of Claims replaces all prior versions in the application:

LISTING OF CLAIMS

1. (Currently Amended) A set of nano-objects, ~~particularly atomic threads, single dimensional nano-structures and quantum dots,~~ this set being characterized in that the nano-objects ~~comprise a metallic element,~~ are in a metallic state, and are formed on the surface of a substrate made of a monocrystalline semiconducting material.
2. (Previously Presented) A set of nano-objects according to claim 1, in which the monocrystalline semiconducting material is chosen from among monocrystalline silicon carbide, monocrystalline diamond, covalent monocrystalline semiconductors, and composite monocrystalline semiconductors.
3. (Previously Presented) A set of nano-objects according to claim 2, in which the substrate is a monocrystalline substrate of silicon carbide in the cubic phase.
4. (Previously Presented) A set of nano-objects according to claim 3, in which the surface is a cubic silicon carbide surface, rich in .beta.-SiC (100) 3x2 silicon.
5. (Previously Presented) A set of nano-objects according to claim 1, in which the nano-objects are three-dimensional clusters of the metallic element on the surface.
6. (Previously Presented) A set of nano-objects according to claim 5, in which the clusters are distributed in an orderly manner on the surface and thus form a lattice.
7. (Previously Presented) A set of nano-objects according to claim 3, in which the surface is a cubic silicon carbide surface which is Si terminated, β -SiC(100) c(4x2), and the nano-objects are parallel atomic threads or parallel single-dimensional nanometric strips.
8. (Previously Presented) A set of nano-objects according to claim 7, in which the surface

comprises parallel atomic threads-of Si, the atomic threads and single dimensional strips being perpendicular to these atomic threads of Si.

9. (Previously Presented) A set of nano-objects according to claim 1, in which the surface comprises passivated areas and non-passivated areas and the nano-objects are formed on these non-passivated areas of the surface.

10. (Previously Presented) A set of nano-objects, particularly atomic threads, single dimensional nano-structures and quantum dots, this set being characterized in that the nano-objects are made essentially of a metal and are formed on the surface of a substrate made of a monocrystalline semiconducting material, in which the metal is chosen from among metals for which the d band is full, jellium type metals, alkaline metals and transition metals.

11. (Previously Presented) A set of nano-objects according to claim 10, in which the metal is chosen from among sodium and potassium.

12. (Withdrawn) Process for making a set of nano-objects, in which a surface (2, 6, 14) of a substrate made of a monocrystalline semiconducting material is prepared, and a metal is deposited on the surface thus prepared.

13. (Withdrawn) Process according to claim 12, in which the monocrystalline semiconducting material is chosen from among monocrystalline silicon carbide, monocrystalline diamond, covalent monocrystalline semiconductors and monocrystalline composite semiconductors.

14. (Withdrawn) Process according to claim 13, in which the substrate is a monocrystalline substrate of silicon carbide in the cubic phase.

15. (Withdrawn) Process according to claim 12, in which the metal is deposited at a temperature greater than or equal to room temperature.

16. (Withdrawn) Process according to claim 14, in which a surface (2) of cubic silicon carbide rich in silicon β -SiC(100) 3×2 is prepared, and the metal is deposited on the surface thus prepared.
17. (Withdrawn) Process according to claim 14, in which a silicon carbide surface (6,14) which is Si terminated β -SiC(100) $c(4 \times 2)$ is prepared, the metal is deposited at room temperature on the surface thus prepared and, by surface migration of metal atoms along lines of Si--Si dimers of the surface $c(4 \times 2)$, atomic threads of the metal are obtained that are parallel to the lines of Si--Si dimers.
18. (Withdrawn) Process according to claim 17, in which a thermal annealing of the substrate is carried out at a temperature below the total desorption temperature of the metal.
19. (Withdrawn) Process according to claim 12, in which the metal is deposited by vacuum evaporation.
20. (Withdrawn) Process according to claim 12, in which the metal is deposited in an inert atmosphere.
21. (Withdrawn) Process according to claim 12, in which passivated areas (15) are formed on the thus prepared surface and the metal is then deposited on non-passivated areas (16, 18) of this surface.
22. (Withdrawn) Process according to claim 12, in which the metal is chosen from among metals for which the d band is full, jellium type metals, alkaline metals and transition metals.
23. (Withdrawn) Process according to claim 22, in which the metal is chosen from among sodium and potassium.
24. (Withdrawn) Process according to claim 17, in which a laser is used to obtain desorption of the metal either by thermal interaction of the beam emitted by this laser over the surface covered with metal, or by desorption of the metal induced by electronic transitions.

25. (Withdrawn) Process according to claim 14, in which the surface is an sp type C terminated surface, namely the β -SiC(100) c(2x2) surface.
26. (Withdrawn) Process according to claim 25, in which this surface comprises atomic lines of sp³ type C and atomic threads of metal are formed that are either parallel or perpendicular to the atomic lines of C.
27. (Withdrawn) Process according to claim 12, in which a lattice of metal dots is formed on the surface of the substrate made of monocrystalline semiconducting material, the substrate material located under the dots is locally transformed and the lattice of dots is eliminated thus to obtain a super-lattice of dots made of the transformed material.
28. (Withdrawn) Process according to claim 27, in which the local transformation of the substrate material is chosen from among oxidation, nitridation and oxynitridation to obtain a super-lattice of dots made of the oxide, nitride or oxynitride of the material.
29. (Withdrawn) Super-lattice of dots, obtained using the process according to claim 28, these dots being made of the oxide, nitride or oxynitride of a monocrystalline semiconducting material and formed at the surface of a substrate of this material.